

FIG. 2B

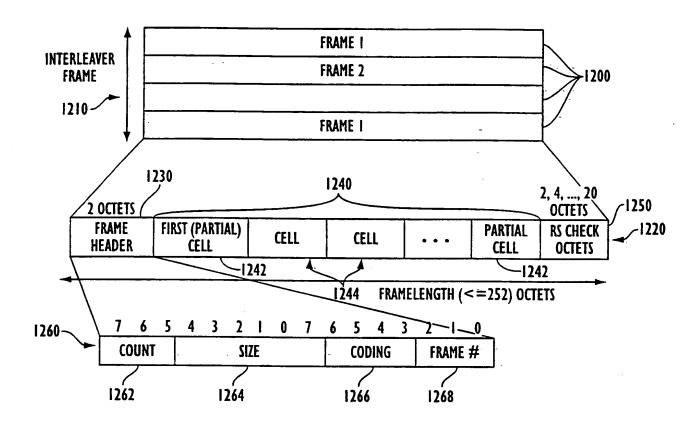
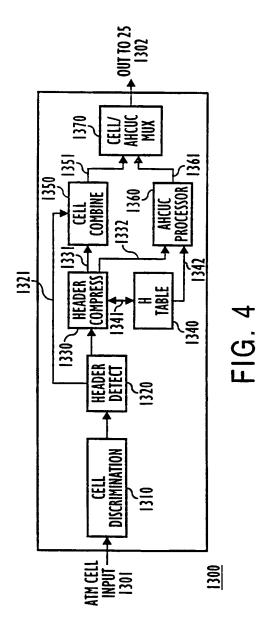


FIG. 3



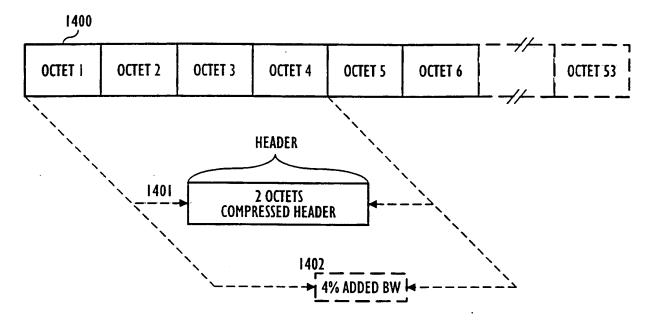


FIG. 5A

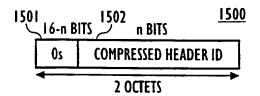


FIG. 5B

ENTRY I 4 OCTET ATM HEADER

ENTRY H-2 4 OCTET ATM HEADER

FIG. 6A

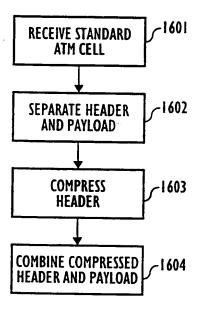
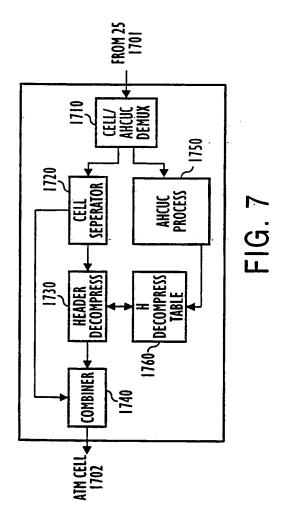


FIG. 6B



ENTRY 0	4 OCTET ATM HEADER
ENTRY I	4 OCTET ATM HEADER
ENTRY H-2	4 OCTET ATM HEADER

FIG. 8A

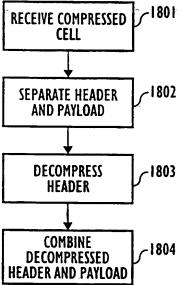
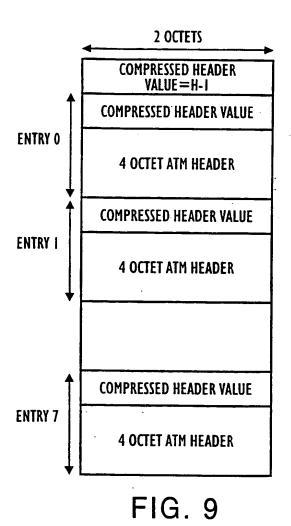


FIG. 8B





SUBSTITUTE SHEET (RULE 26)

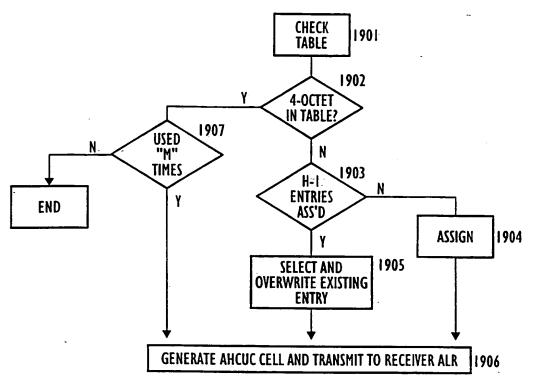


FIG. 10A

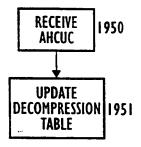


FIG. 10B

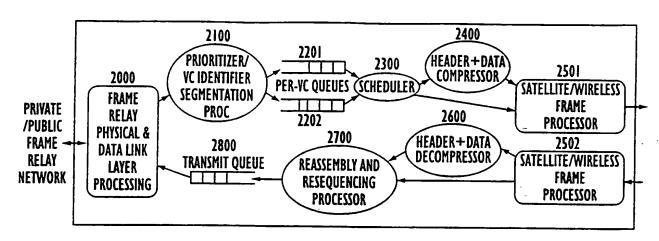


FIG. 11A

VC Id PACKET # SEQ # LAST
PAYLOAD DATA

FIG. 11B

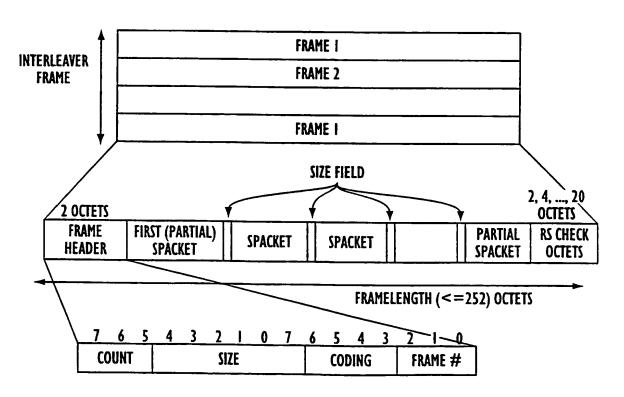


FIG. 11C

SUBSTITUTE SHEET (RULE 26)